<u>AMENDMENTS TO THE SPECIFICATION</u>

Please add the following paragraph on page 21 of the present application between the paragraph which reads "Figure 11 is a block diagram... shown in the microprocessor of Figure 3" and the paragraph which reads "Notation And Nomenclature":

Figure 12 illustrates a method of protecting a memory address holding translated code and invalidating code translation in accordance with one embodiment of the present invention.